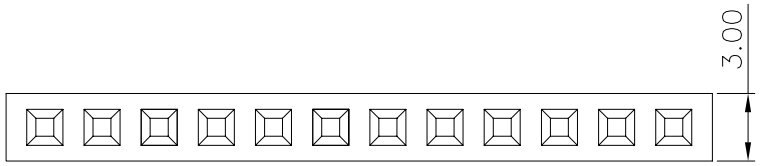
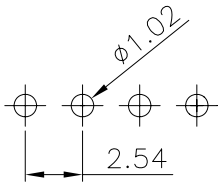
REV.	LOCAS.	DESCRIPTION	DATE	DRAWN
01		INITIAL RELEASE.	2007.09.15	张 荣



Recommended P.C.Board
Hole Layout

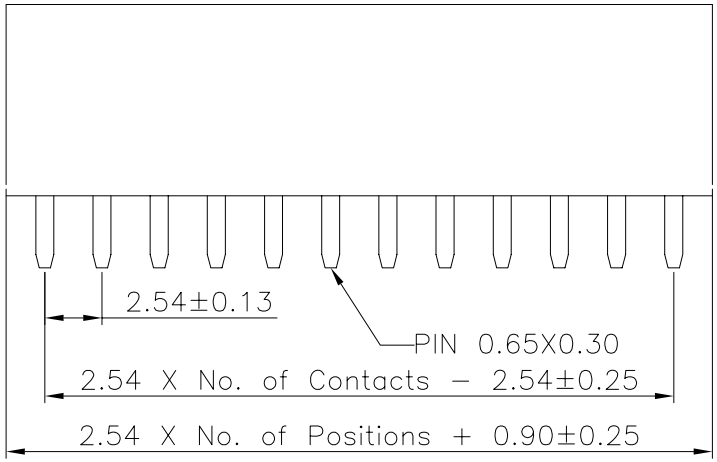


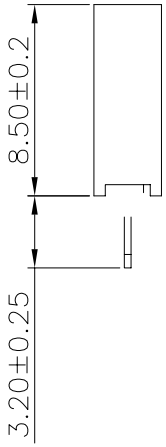
Specifications

Current Rating: 3Amp
Insulation Resistance: 1000MΩ Min.
Dielectric Withstanding: 600V AC/DC
Contact Resistance: 20mΩ Max.
Operating Temperature:−40°C to +105°C

Materials

Contact Material: Phosphor Bronze
Insulation Material: Polyester, UL 94V−0
P.B.T
All materials must be GP materials





Ordering Information

FHD101 − XX XX A 01 − X

- XX: No. of Pins per Row 02~40
- XX: Contact Plating
  - 01=Tin
  - 02=Gold Flash
  - Duplex plating
    - 05=Gold Flash /Tin
    - …
- X: Options
  - P=cap
  - T=tape reel
  - C=acetate tape
  - PT=cap+tape reel

深圳市硕康达科技有限公司 ShenZhenShi Sokconn Technology Co.,Ltd	GENERAL TOLERANCE		DESIGN: 张 荣	DATE: 2007.09.15	DWG. NO. FHD101−A01	TITLE. 2.54 FEMALE HEADER SINGLE ROW DIP TYPE BASE 8.50H
	X.° ± 5.0°	X. ± 0.30	CHECKED:	DATE:		
	.X° ± 3.0°	.X ± 0.25	AUDITED:	DATE:	PART NO. FHD101−XX XX A01−X	
	.XX° ± 2.0°	.XX ± 0.15	APPROVED:	DATE:		
	.XXX° ± 1.0°	.XXX ± 0.10				
REV. 01 / UNIT. mm / SCALE. 5:1 / PAGE. 1/1						