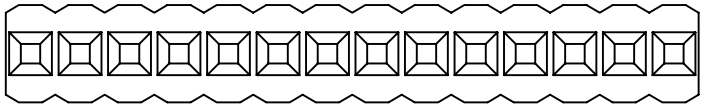
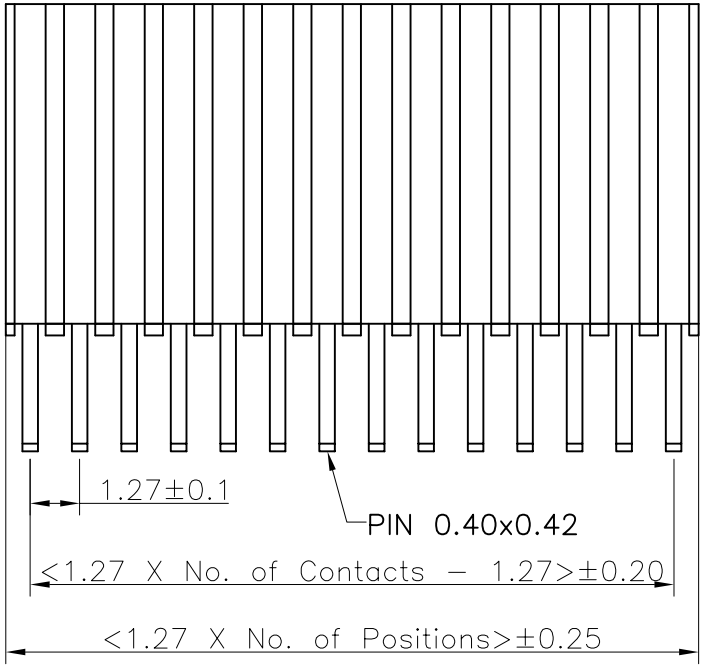
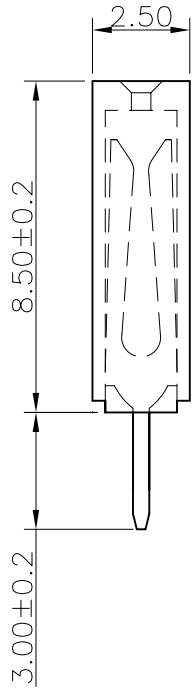
REV.	LOCAS.	DESCRIPTION	DATE	DRAWN
01		INITIAL RELEASE.	2012.07.20	尹 晖



Recommended P.C.Board
Hole Layout
(Tolerance:±0.05)





1.Specifications

Current Rating: 1Amp
Insulation Resistance: 1000M Ω Min.
Dielectric Withstanding: 300V AC/DC
Contact Resistance: 20mΩ Max.
Operating Temperature:−40°C to +105°C

2.Materials

Contact Material: Phosphor Bronze
Insulation Material: Polyester, UL 94V−0
NYLON−6T
All materials must be GP materials

Ordering Information

FED108 − XX XX A 01 − X

- XX: No. of Pins per Row 01~50
- XX: Contact Plating
  - 01=Tin
  - 02=Gold Flash
  - Duplex plating
    - 05=Gold Flash /Tin
    - …
- X: Options
  - P=CAP
  - T=T&R
  - PT=CAP+T&R

深圳市硕康达科技有限公司
ShenZhenShi Sokconn Technology Co.,Ltd

GENERAL TOLERANCE	
X.° ± 5°	X. ± 0.30
.X° ± 2°	.X ± 0.20
.XX° ± 1°	.XX ± 0.15
.XXX° ± 0.5°	.XXX ± 0.10

REV.	UNIT.	SCALE.	PAGE.
01	mm	1:1	1/1

DESIGN: 尹 晖	DATE: 2012.07.20	DWG. NO. FED108−A01	TITLE. 1.27 FEMALE HEADER SINGLE ROW DIP TYPE BASE 8.50
CHECKED:	DATE:		
AUDITED:	DATE:	PART NO. FED108−XX XX A01−X	
APPROVED:	DATE:		