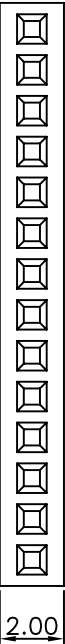
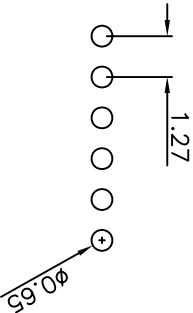


REV.	LOCAS.	DESCRIPTION	DATE	DRAWN
01		INITIAL RELEASE.	2007.08.02	张 荣



Recommended P.C.Board
Hole Layout



Specifications

Current Rating: 1Amp

Insulation Resistance: 1000M Min.

Dielectric Withstanding: 300V AC

Contact Resistance: 20mΩ Max.

Operating Temperature: -40°C to +105°C

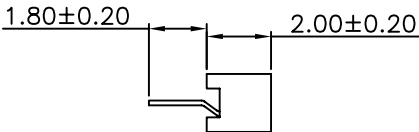
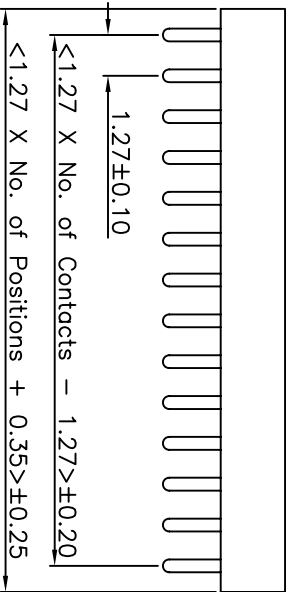
Materials

Contact Material: Phosphor Bronze

Insulation Material: Polyester, UL 94V-0

Nylon-6T

All materials must be GP materials



Ordering Information

FED102 - XX XX A 01 - X

No. of Pins per Row
02~50

Contact Plating
01=Tin
02=Gold Flash
Duplex plating
05=Gold Flash
Options
P=CAP
T=T&R
PT=CAP+T&R
C=acetate tape
Tc=acetate tape+tape reel



深圳市硕康达科技有限公司
ShenzhenShi Sokcom Technology Co., Ltd

GENERAL TOLERANCE

DESIGN: 张 荣

DATE: 2007.08.02

DWG. NO. FED102-A01

TITLE:

X. ± 5° X. ± 0.30

CHECKED:

DATE:

PART NO.

1.27 FEMALE HEADER

.X ± 2° .X ± 0.20

AUDITED:

DATE:

FED102-XX XX A01-X

SINGLE ROW DIP TYPE

.XX ± 1° .XX ± 0.15

APPROVED:

DATE:

BASE 2.00H

.XXX ± 0.5° .XXX ± 0.10